REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
	3	Circuit change, added parts	08/21/09	MS

NOTE 1,
TO IMPLEMENT DCR SENSING ON PHASE 1, SHORT RS1.
REMOVE R29 AND R30. STUFF R58 WITH A 0 OHM RESISTOR.
THE DCR SENSE FILTER CONSISTS OF C14, R56 AND R57.
CALCULATE THESE VALUES PER THE DATA SHEET.

NOTE 4,
ONLY APPLY LOAD FROM J3 TO J4 FOR VOUT1
ONLY APPLY LOAD FROM J5 TO J6 FOR VOUT2
DO NOT CONNECT SCOPE GROUND TO E14

NOTE 2,
TO IMPLEMENT DCR SENSING ON PHASE 2, SHORT RS2.
REMOVE R39 AND R40. STUFF R62 WITH A 0 OHM RESISTOR.
THE DCR SENSE FILTER CONSISTS OF C15, R59 AND R60.
CALCULATE THESE VALUES PER THE DATA SHEET.

NOTE 3,
FOR VOUT2 >= 3.3V, REMOVE R11 AND R70. STUFF R12
AND R69 WITH 0 OHM RESISTORS.

OPTIONAL JUMPERS FOR SINGLE OUTPUT/DUAL PHASE OPERATION

TEMPERATURE COMPENSATION NETWORK FOR DCR SENSING

X1, X2 Represents Tooling Holes on PCB

CONTRACT NO.

APPROVALS

PCB DES. MI

ENG. MS

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TITLE: SCHEMATIC

DUAL OUTPUT SYNCHRONOUS BUCK CONVERTER

SIZE	IC NO.	REV
	LTC3855EUJ DEMO CIRCUIT 1441A	3

DATE: Friday, November 20, 2009 SHEET 1 OF 1